

Government Engineering College, Dahod
Computer Department
3rd Semester
Digital Electronics
Mid Semester Syllabus

All Students are hereby informed that questions from following units (As per GTU Syllabus) will be asked in mid semester exam for the subject of Digital Electronics.

Unit	Topics
1	Binary Systems and Logic Circuits: The Advantage of Binary, Number Systems, The Use of Binary in Digital Systems, Logic Gates, Logic Family Terminology.
2	Boolean Algebra and Mapping Methods: Boolean Algebra, Karnaugh Maps, Variable Entered Maps, Realizing Logic Function with Gates, Combinational Design Examples
3	Logic Function Realization with MSI Circuits: Combinational Logic with Multiplexers and Decoders, Standard Logic Functions with MSI Circuits, Design Problem Using MSI Circuits.