Government Engineering College, Dahod.
Electro. & Commu. Engg. Dept.
Program for <u>Backlog Remedial</u> Examination, Oct 2020
Semester 4th /6th/8th

Semester	Subject Name	Subject Factulty	Date	Time
4th Sem	2141001-Microprocessor and Interfacing	2.00 P.M. TO 3.30 P.M.	19-10-2020	A.B.Pasaya
	2141002-Analog Circuit Design	2.00 P.M. TO 3.30 P.M.	20-10-2020	C.B.Pagi
	2141003-Electronics Measurement and Instrumentation	2.00 P.M. TO 3.30 P.M.	21-10-2020	S.M.Vaishnav
	2141004-Control System Engineering	2.00 P.M. TO 3.30 P.M.	22-10-2020	Y.R.Chaudhari
	2141005-Signals and Systems	2.00 P.M. TO 3.30 P.M.	23-10-2020	S.N.Damor
4th Sem	3141002-Analog Circuit Design	11.00 A.M. TO 12.30 P.M.	19-10-2020	C.B.Pagi
	3141005-Signal & Systems	11.00 A.M. TO 12.30 P.M.	20-10-2020	S.N.Damor
	3141007-Professional Ethics	11.00 A.M. TO 12.30 P.M.	21-10-2020	S.M.Vaishnav
	3141008-Microprocessor & Microcontroller	11.00 A.M. TO 12.30 P.M.	22-10-2020	A.B.Pasaya
	3141009-Electromagnetic Theory	11.00 A.M. TO 12.30 P.M.	23-10-2020	B.M.Parmar
6th Sem	2161101- VLSI Technology & Design	11.00 A.M. TO 12.30 P.M.	19-10-2020	A.B.Pasaya
	2161001- Digital Communication	11.00 A.M. TO 12.30 P.M.	20-10-2020	B.M.Parmar
	2161005- Optical Communication	11.00 A.M. TO 12.30 P.M.	21-10-2020	S.M.Vaishnav
	2161006- Power Electronics Devices and Circuits	11.00 A.M. TO 12.30 P.M.	22-10-2020	C.B.Pagi
	2161003-Antenna & Wave Propagation	11.00 A.M. TO 12.30 P.M.	23-10-2020	S.N.Damor
8th Sem	(2181102) Fundamentals of Image Processing	11.00 A.M. TO 12.30 P.M.	19-10-2020	C.B.Pagi

➢ Student who have backlog in above mentioned subject are strictly informed to contact respective subject faculty.

➢ It will be the responsibility of student if they do not appear in the exam or contact subject faculty.

➢ Exam will be conducted in **online mode Using MS Teams Platform.**

➢ **Regarding <u>Internal teamwork/submission</u>, if any Students fail or absent in above mentioned subjects of semester 4th/6th/8th, then contact to consult subject faculty and complete before <u>23/10/2020</u> without fail.**

Head Of Department

Electronics and Communication Engg.